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DATA LINE FILTER ENQUIRY FORM

Description of Requirement	Enquiry Reference

LOW PASS FILTER DESIGN PARAMETERS
(Please provide as much information as possible)

Voltage V AC / DC Hz	No of Lines
Current mA	Approx size
Analogue / Digital	Temp Range EC to EC
Impedance line to line Ω	Impedance line to earth Ω
Type of line: co-ax / twisted pair / other	Max tolerable dc resistance per line
Max tolerable capacitance per line	Max tolerable inductance per line

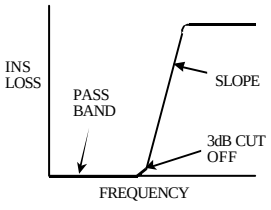
Modem details (if applicable)

Assym Insertion Loss in Ω system	Freq								
	dB								

SPECIAL REQUIREMENTS (where applicable)

Box style	Transient suppression
Environmental	Terminals
Return Loss	Balance
Crosstalk	Specifications

GUIDANCE NOTES



1. Flat pass band needs an accurate impedance match
2. Guide for digital data - pass band to 3dB cut off = 10x data rate
(This guide usually works in practice for most good quality data circuits. However, because the design of data circuitry and its susceptibility to filter capacitance and inductance are outside our control, we would recommend a compatibility check.
We can often help with a loan sample for test purposes)
3. Slope is 6dB per octave (doubling of frequency) per filter element (Cap or inductor)
4. 7 element circuits are normally the most practical but up to 11 elements are possible
5. Feedthrough capacitors are essential for good performance above 1MHz
6. Low pass designs with discrete components are suitable for up to 30MHz pass band or 2MB/s data

Potential Quantity	Target Price
Enquiry Taken by	Date
Sales Action	
Engineering Action	